

Enter WIRCE 10-17-05

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

HIRASE, Masaki et al.

Serial No.: 09/908,941

Filed: July 20, 2001

Group Art Unit: 2812

Examiner: Jennifer M. Kennedy

P.T.O. Confirmation No.: 1043

For. SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE

SAME

PRELIMINARY SUBMISSION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Date: October 17, 2005

Sir:

Prior to examination, please amend the above-identified application as set forth below:

Amendments to the Claims begin on page two of this paper.

Remarks begin on page 12 of this paper.